

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

ISHIDA

Serial No.: 09/730,875

Filed: December 5, 2000

Atty. File No.: 2933SE-64-CON

For: "THIN FILM TRANSISTOR WITH
GATE ELECTRODE HAVING
TAPERED END PORTIONS"
(AS AMENDED)



) Group Art Unit: 2811

) Examiner: S. Loke

) REQUEST FOR EXTENSION OF TIME

CERTIFICATE OF MAILING

I HEREBY CERTIFY THAT THIS CORRESPONDENCE IS
BEING DEPOSITED WITH THE UNITED STATES
POSTAL SERVICE AS FIRST CLASS MAIL IN AN
ENVELOPE ADDRESSED TO ASSISTANT
COMMISSIONER OF PATENTS, WASHINGTON, DC
20231 ON April 24, 2002.

SHERIDAN ROSS P.C.
BY: *Staged Borenkamp*

Commissioner of Patents
Washington, D.C. 20231

Dear Sir:

Applicant, through his attorneys, respectfully petition for an extension of time under 37 CFR § 1.136(a) of three (3) months to respond to the Office Action mailed on October 25, 2001, with respect to the above-identified application, thereby extending the period for response from January 25, 2002, to April 25, 2002.

Enclosed is a check in the amount of \$460.00 as payment for the extension fee. Please credit any overpayment or debit any underpayment to Deposit Account No. 19-1970.

Respectfully submitted,

SHERIDAN ROSS P.C.

By: *Kenneth C. Winterton*

Kenneth C. Winterton
Registration No. 48,040
1560 Broadway, Suite 1200
Denver, Colorado 80202-5141
(303) 863-9700

Date: April 24, 2002

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